

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	5	"475486".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/17 10:38
S2	4	"475497".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/16 15:38
S3	0	(song-hongjiang.in. and ((delay adj2 lock\$3 adj2 loop) same processor)) and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:31
S4	1	song-hongjiang.in. and ((delay adj2 lock\$3 adj2 loop) same processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/17 12:07
S5	4	(intel adj corporation.as.) and ((delay adj2 lock\$3 adj2 loop) same processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:37
S6	4	((intel adj corporation.as.) and ((delay adj2 lock\$3 adj2 loop) same processor)) and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/16 15:41
S7	36	(intel adj corporation.as.) and ((delay adj2 lock\$3 adj2 loop) and processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/16 15:41
S8	31	((intel adj corporation.as.) and ((delay adj2 lock\$3 adj2 loop) and processor)) and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/16 15:53
S9	1	(intel adj corporation.as.) and ((delay adj2 lock\$3 adj2 loop) with processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/16 15:52
S10	91	processor with (delay adj2 lock\$3 adj2 loop)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/17 10:59
S11	70	(processor with (delay adj2 lock\$3 adj2 loop)) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/16 15:53
S12	47	((processor with (delay adj2 lock\$3 adj2 loop)) and memory) and computer.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/06/16 15:53

S37	547	processor same dll	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/17 16:59
S38	10	S37 and 375/376.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/17 17:00
S39	2	S38 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 11:30
S40	7	"645676".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 11:30
S41	2	"6456676".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 11:38
S42	2	"20020130691"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 09:00
S43	0	(song-hongjiang.in. and ((delay adj2 lock\$3 adj2 loop) with processor)) and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:31
S44	0	(song-hongjiang.in. and ((lock\$3 adj2 loop) with processor)) and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:32
S45	0	(song-hongjiang.in. and ((pll or dll) with processor)) and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:32
S46	104	(intel adj corporation.as.) and ((pll or dll) with processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:53
S47	71	S46 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:53
S48	0	S47 and (cpu and (micro adj2 processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:41

S49	38	S47 and (cpu and (microprocessor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/08 14:41
S50	17	(intel adj corporation.as.) and ((pll or dll) near processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 07:43
S51	14	S50 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 07:44
S52	0	(intel adj corporation.as.) and ((pll and dll) near processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 07:43
S53	23	((pll and dll) with processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 09:29
S54	17	S53 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 09:29
S55	0	327/158.ccls. and ((pll and dll) with processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 09:00
S56	0	"20020130691" and pll and dll	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 09:00
S57	0	713/322.ccls. and ((pll and dll) with processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 09:29
S58	7	713/322.ccls. and ((pll and dll) and processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:05
S59	7	S58 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:06
S60	23	processor with (dll and pll)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:12

S61	17	S60 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:12
S62	0	processor near (dll and pll)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:12
S63	57	processor same (dll and pll)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:24
S64	33	S63 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:25
S65	26	microprocessor\$1 with (dll and pll)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 14:08
S66	13	S65 and @ad<"20010329"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 10:25
S67	0	375/354.ccls. and (microprocessor\$1 with (dll and pll))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 14:08
S68	1	375/354-376.ccls. and (microprocessor\$1 with (dll and pll))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 14:08